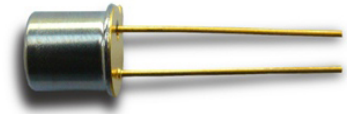




## EPD-470-0-1.4



### TECHNICAL DATA

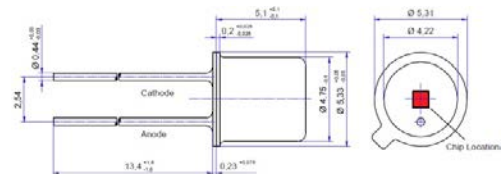
## Selective Photodiode, TO-18 package

GaP

EPD-470-0-1.4 is a GaP based photodiode, mounted into a TO-18 housing. This device is featuring a narrow bandwidth and reduced spectral sensitivity at 400 – 500 nm.

### Specifications

- Spectral Responsivity (Peak): 470 nm
- Active Area: 1.76 mm<sup>2</sup>
- Package: TO-18



(Unit: mm)

### Parameters (T<sub>A</sub>=25°C)

Item	Symbol	Value	Unit
Active Area	A	1.76	mm <sup>2</sup>
Temperature Coefficient of I <sub>Ph</sub>	T <sub>C</sub> (I <sub>Ph</sub> )	0.15	%/K
Temperature Coefficient of I <sub>D</sub>	T <sub>C</sub> (I <sub>D</sub> )	1.05	1/K
Operating Temperature	T <sub>opr</sub>	-40 ... +125	°C
Storage Temperature	T <sub>stg</sub>	-40 ... +125	°C
Soldering Temperature * <sup>1</sup>	T <sub>sol</sub>	240	°C

\*<sup>1</sup> must be completed within 3 seconds at 240°C

### Electro-Optical Characteristics

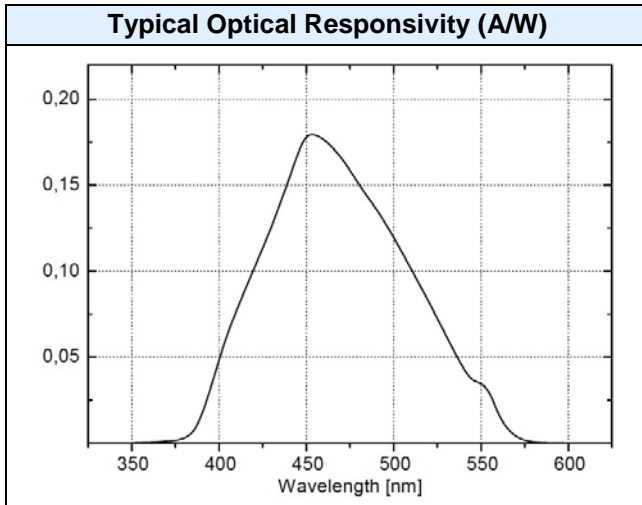
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse Voltage * <sup>1</sup>	V <sub>R</sub>	I <sub>R</sub> =10μA	5	-	-	V
Dark Current (E <sub>e</sub> =0W/m <sup>2</sup> )	I <sub>D</sub>	V <sub>R</sub> =5V	-	5	30	pA
Central Sensitivity Wavelength	λ <sub>C</sub>	V <sub>R</sub> =0V	460	470	480	nm
Responsivity at λ <sub>C</sub>	S <sub>λ</sub>	V <sub>R</sub> =0V	-	0.18	-	A/W
Spectral Range at 0.5 max.	λ <sub>0.5</sub>	V <sub>R</sub> =0V	425	-	525	nm
Sensitivity Range at 1% * <sup>1</sup>	λ <sub>min</sub> , λ <sub>max</sub>	V <sub>R</sub> =0V	380	-	570	nm
Spectral Bandwidth at 50%	Δλ <sub>0.5</sub>	V <sub>R</sub> =0V	-	100	-	nm

\*<sup>1</sup> information only

**Note:** The above specifications are for reference purpose only and subjected to change without prior notice.



## Typical Performance Curves



## Precaution for Use

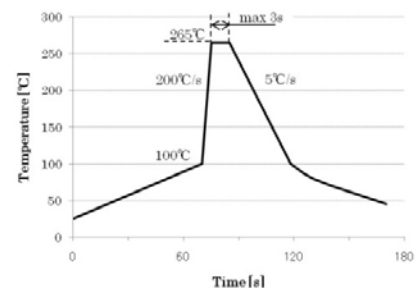
### 1. Lead Forming

- Lead forming should be done before soldering.
- When forming leads, the leads should be bent at a point at least 3 mm from the base of the lead. DO NOT use the base of the lead frame as a fulcrum during lead forming!
- DO NOT apply any bending stress to the base of the lead. The stress to the base may damage the PD's characteristics or it may break the PDs.
- When mounted the PDs onto the printed circuit board, the holes on the circuit board should be exactly aligned with the leads of PDs. If the PDs are mounted with stress at the leads, it causes deterioration of the lead and it will degrade the PDs.

### 2. Soldering Conditions

- Solder the PDs no closer than 3 mm from the base of the lead.
- DO NOT apply any stress to the lead particularly when heat.
- After soldering the PDs, the lead should be protected from mechanical shock or vibration until the PDs return to room temperature.
- The PDs must not be reposition after soldering.
- When it is necessary to clamp the PDs to prevent soldering failure, it is important to minimize the mechanical stress on the PDs.
- Cut the PD leads at room temperature. Cutting the leads at high temperature may cause the failure of the PDs.

### Soldering Conditions



### 3. Static Electricity

- The PDs are very sensitive to Static Electricity and surge voltage. So it is recommended that a wrist band and/or an anti-electrostatic glove be used when handling the PDs.
- All devices, equipment and machinery must be grounded properly. It is recommended that precautions should be taken against surge voltage to the equipment that mounts the PDs.

